

Consulting on standalone QSFP-DD switches

These 200G Q-DD AOCs automatically configure a Q-DD port to operate in 4-channel QSFP56 mode. Because of the 2xQSFP28 ends, this limits the Q-DD port to 200G (2 channels of 100G each).

July 11, 2019 - QSFP-DD Hardware Specification for QSFP DOUBLE DENSITY 8X PLUGGABLE TRANSCEIVER - Rev 5.0 May 8, 2019 - Common Management Interface Specification - Rev 4.0

Complete QSFP-DD compatibility guide with switch matrix, firmware requirements, and MSA compliance. Verify module compatibility before you buy.

QSFP-DD ports incorporate a riding heatsink that can be sized independently of the optical module, added on top of the module, or placed between modules. This flexibility enables switch and routing ...

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Powered by Greylock and Delphi DSP ASICs, and silicon photonic integrated circuits (PICs) for an optimized co-packaged design with 3D Siliconization. Supports an expansive list of interoperability ...

Arista's 100G/port SFP-DD and DSFP systems are quad-rate systems, enabling the use of 10G, 25G and 50G SFP optics and cables, as well as 100G SFP-DD / DSFP cables - enabling one system to ...

The QSFP-DD family supports legacy QSFP channels on the front interface and four additional channels on the rear interface. This interconnect system optimizes density and power ...

Additional Learning Hub Resources What is the difference between PoE vs PoE+ vs PoE++ Switch? How to choose an appropriate optical fiber jumper for the QSFP-DD 800G optical ...

Find which switches support QSFP-DD modules. Our 2025 guide covers Cisco, Arista, Juniper and NVIDIA platforms with backward compatibility tips.

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